CARRY OUTPUTS	Provided internally for each channel, -lv into 50 ohms.
MANUAL RESET	A pushbutton resets all channels and overflow flip-flops.
GATE SELECT	A locking toggle switch allows the gates to be controlled by the front panel inhibit signals or by this signal "OR"ed with the dataway inhibit (I).
CAMAC COMMANDS	
N•FO•A <sub>i</sub> *	Reads 24 bits of data onto Read Lines 1-24. Generates a Q response. Resets overflow at S2.
$N \cdot Fl \cdot A14$	Reads LAM requests on R1-4. Generates Q response.
$N \cdot F2 \cdot A_{i} \star$	Same as FO and in addition resets channel to zero at S2.
N•F8•A14	Tests LAM (overflow) requests and if enabled generates a response on the $\it Q$ line.
N•F9•A <sub>i</sub> *	Resets counter channel at S2.
N•F10•A <sub>i</sub> *	Resets overflow flip-flop at S2.
N•F24•A14	Disables LAM requests.
N•F25•A0-3	Increments each channel by one at S2 (independent of inhibit gates).
N•F26•A14	Enables LAM requests.
*NOTE: $A_{i}$ $i = 0, 1, 2, 3$	
$Z \cdot S2$	Resets all channels, overflow flip-flop, disables LAM requests.
$C \cdot S2$	Resets all channels and overflow flip-flops.
I	Inhibits all channels if gate select switch is in appropriate position.
Q	A Q=1 is generated in response to F0, F1, F2, and F8 if an overflow has occured and the unit is enabled.
L	An "L" signal is generated whenever an overflow has occured and the module is enabled, but not addressed.
X	X=1 is generated for all valid commands.
INDICATORS	
Gate Lights	Four LED's are provided to indicate when the counting gates are open.
N Light	An LED is provided to indicate when the module is addressed.
POWER REQUIREMENTS	800ma, +6v; 400ma, -6v

TEMPERATURE RANGE

0°C to 50°C

SIZE

Single width CAMAC module

CONNECTORS

LEMO RA00250, Mating Connector F00250

OPTION

125MHZ Scaler - This unit has a counting speed of 125MHZ,

all other specifications are the same. Model S1

JEI 0182

CARRY OUTPUTS Provided internally for each channel, -lv into 50 ohms. MANUAL RESET A pushbutton resets all channels and overflow flip-flops. A locking toggle switch allows the gates to be controlled by GATE SELECT the front panel inhibit signals or by this signal "OR"ed with the dataway inhibit (I). CAMAC COMMANDS  $N \cdot FO \cdot A_i *$ Reads 24 bits of data onto Read Lines 1-24. Generates a Q response. Resets overflow at S2. N•F1•A14 Reads LAM requests on R1-4. Generates Q response. Same as FO and in addition resets channel to zero at S2. N • F2 • A; \* Tests LAM (overflow) requests and if enabled generates a N • F8 • A14 response on the O line.  $N \cdot F9 \cdot A_i *$ Resets counter channel at S2. N • F10 • Ai \* Resets overflow flip-flop at S2. N•F24•A14 Disables LAM requests. N•F25•A0-3 Increments each channel by one at S2 (independent of inhibit gates). N•F26•A14 Enables LAM requests. \*NOTE:  $A_i i = 0, 1, 2, 3$ Resets all channels, overflow flip-flop, disables LAM requests.  $Z \cdot S2$ C.S2 Resets all channels and overflow flip-flops. T Inhibits all channels if gate select switch is in appropriate position. A Q=1 is generated in response to F0, F1, F2, and F8 if an overflow has occured and the unit is enabled. An "L" signal is generated whenever an overflow has occured Т. and the module is enabled, but not addressed. X X=1 is generated for all valid commands. INDICATORS Gate Lights Four LED's are provided to indicate when the counting gates are N Light An LED is provided to indicate when the module is addressed.

POWER REQUIREMENTS 800ma, +6v; 400ma, -6v

TEMPERATURE RANGE 0°C to 50°C

SIZESingle width CAMAC module

CONNECTORS LEMO RA00250, Mating Connector F00250

OPTION 125MHZ Scaler - This unit has a counting speed of 125MHZ,

all other specifications are the same. Model S1

JET 0182